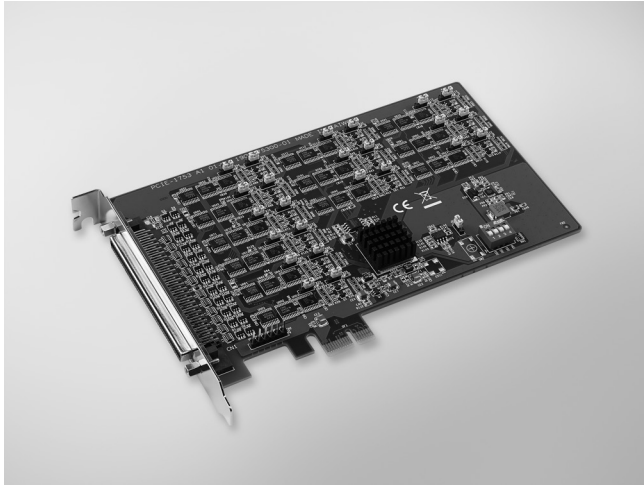


PCIE-1753

96-ch Digital I/O PCI Express Card



FCC CE RoHS
COMPLIANT
2002/95/EC

Features

- Fully compatible with PCI-1753
- Up to 96 TTL digital I/O lines
- Emulates mode 0 of 8255 PPI
- Buffered circuits for higher driving capacity than the 8255
- Interrupt handling capability
- Output status read-back
- Supports input data pattern match and change of state functions
- Keep I/O settings and output status after hot reset
- Supports both dry and wet contact
- High-density 100-pin SCSI connector

Introduction

The PCIE-1753 is a 96-bit digital I/O card for the PCI bus. The card emulates mode 0 of the 8255 PPI chip, but the buffered circuits offer higher driving capability than the 8255. The 96 I/O lines are divided into twelve 8-bit I/O ports. Users can configure each port as input or output via software.

Specifications

Digital Input/Output

- **Channels** 96 digital I/O lines
- **Programming Mode** 8255 PPI mode 0
- **Compatibility** 5 V/TTL
- **Input Voltage** Logic 0: 0.8 V max.
Logic 1: 2.0 V min.
- **Output Voltage** Logic 0: 0.4 V max.
Logic 1: 2.4 V min.
- **Output Capability** Current to drive single channel: 24 mA (max.)
Current to drive all channels: 1.44 A (max.)

General

- **Bus Type** PCI Express V1.0
- **I/O Connector** 1 x 100-pin SCSI female connector
- **Dimensions (L x H)** 175 x 100 mm (6.9" x 3.9")
- **Power Consumption** Typical: 3.3 V @ 600 mA
Max.: 3.3 V @ 3 A
- **Operating Temperature** 0 ~ 60°C (32 ~ 140°F) (refer to IEC 68-2-1, 2)
- **Storage Temperature** -20 ~ 70°C (-4 ~ 158°F) (refer to IEC 68-2-3)
- **Storage Humidity** 5 ~ 95% RH, non-condensing

Ordering Information

- **PCIE-1753** 96-ch Digital I/O PCI Express Card

Accessories

- **ADAM-3968** 68-pin DIN-rail SCSI Wiring Board
- **ADAM-3968/20** 68-pin SCSI to 3 20-pin Box Header Board
- **ADAM-3968/50** 68-pin SCSI to 2 50-pin Box Header Board
- **PCLD-8751** 48-ch Isolated Digital Input Board
- **PCLD-8761** 24-ch Replay/ Isolated Digital Input Board
- **PCLD-8762** 48-ch Relay Board
- **PCL-10268** 100-pin to Two 68-pin SCSI Cables, 1 m and 2 m

Pin Assignments

P0_0	1	51	P6_0	P0_0-7: I/O pins of Port 0
P0_1	2	52	P6_1	P1_0-7: I/O pins of Port 1
P0_2	3	53	P6_2	P2_0-7: I/O pins of Port 2
P0_3	4	54	P6_3	P3_0-7: I/O pins of Port 3
P0_4	5	55	P6_4	P4_0-7: I/O pins of Port 4
P0_5	6	56	P6_5	P5_0-7: I/O pins of Port 5
P0_6	7	57	P6_6	P6_0-7: I/O pins of Port 6
P0_7	8	58	P6_7	P7_0-7: I/O pins of Port 7
P1_0	9	59	P7_0	P8_0-7: I/O pins of Port 8
P1_1	10	60	P7_1	P9_0-7: I/O pins of Port 9
P1_2	11	61	P7_2	P10_0-7: I/O pins of Port 10
P1_3	12	62	P7_3	P11_0-7: I/O pins of Port 11
P1_4	13	63	P7_4	GND: Ground
P1_5	14	64	P7_5	VCC: +5V voltage output (0.5 A max.)
P1_6	15	65	P7_6	
P1_7	16	66	P7_7	
P2_0	17	67	P8_0	
P2_1	18	68	P8_1	
P2_2	19	69	P8_2	
P2_3	20	70	P8_3	
P2_4	21	71	P8_4	
P2_5	22	72	P8_5	
P2_6	23	73	P8_6	
P2_7	24	74	P8_7	
GND	25	75	GND	
P3_0	26	76	P9_0	
P3_1	27	77	P9_1	
P3_2	28	78	P9_2	
P3_3	29	79	P9_3	
P3_4	30	80	P9_4	
P3_5	31	81	P9_5	
P3_6	32	82	P9_6	
P3_7	33	83	P9_7	
P4_0	34	84	P10_0	
P4_1	35	85	P10_1	
P4_2	36	86	P10_2	
P4_3	37	87	P10_3	
P4_4	38	88	P10_4	
P4_5	39	89	P10_5	
P4_6	40	90	P10_6	
P4_7	41	91	P10_7	
P4_0	42	92	P11_0	
P4_1	43	93	P11_1	
P4_2	44	94	P11_2	
P4_3	45	95	P11_3	
P4_4	46	96	P11_4	
P4_5	47	97	P11_5	
P4_6	48	98	P11_6	
P4_7	49	99	P11_7	
VCC	50	100	VCC	